Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2127	455/557.ccls. or 455/556.1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/27 12:30
S2	204	S1 and modulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/27 12:30
S5	151	S2 and demodulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/27 12:31
S6	2127	455/557.ccls. or 455/556.1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:25
S7	1974	S6 and (digital signal processor or DSP)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:26
S8	1592	S7 and (application processor or AP)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:26
S9	312	S6 and (digital adj2 signal adj2 processor or DSP)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:33
S10	7	S9 and (application adj2 processor or AP)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:33

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S11	82607	(digital adj2 signal adj2 processor) or DSP	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:33
S12	2298	S11 and ((application adj2 processor) or AP)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:33
S13	163	S12 and master adj2 control\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:34
S14	146182	S13 and "455"/\$.ccls. or "375"/\$. ccls. or "370"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:35
S15	25	S13 and ("455"/\$.ccls. or "375"/\$. ccls. or "370"/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:35
S16	2	("20050066074").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/29 13:43
S17	2131	455/557.ccls. or 455/556.1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 09:25
S18	672	S17 and (image or camera)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 09:26

						
S19	105	S18 and flash adj2 memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 09:26
S20	432	S17 and camera	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 09:26
S21	74	S20 and flash adj2 memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 09:26
S22	0	S17 and (memory near light)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 10:56
S23	1484	(memory near light)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:00
S24	43	S23 and modem	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 10:57
S25	0	(memory near strobe adj2 light)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:03
S26	461	SDRAM and data adj2 banks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:04

S27	0	SDRAM near protection adj2 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:05
S28	1	SDRAM near protection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:05
S29	19387	SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:15
S30	40	SDRAM near (protect\$3 or secur\$4 or guard\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:20
S31	167	SDRAM same (sam\$1 adj2 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:17
S32	17	S31 and data adj2 banks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:17
S33	648	SDRAM same (protect\$3 or secur\$4 or guard\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:21
S34	105	S33 same address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:44

S35	19387	SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:34
S36	1620	S35 and sam\$1 adj2 address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:49
S37	0	S35 and (sam\$1 adj2 address near protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 11:49
S38	70	S35 and (sam\$1 adj2 address same protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 13:21
S39	10	SDRAM same (protect\$3 or secur\$3 or guard\$3) adj2 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:28
S40	145	SDRAM and (address near protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:35
S41	1	SDRAM same (address near protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:36
S42	2	("6378102").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:43

S43	16	SDRAM same protect\$3 adj2 signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:46
S44	2	("20020040437").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:56
S45	2	("6378102").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 12:56
S46	522	memory and (sam\$1 adj2 address same protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 13:22
S47	250	memory same (sam\$1 adj2 address same protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/30 13:22